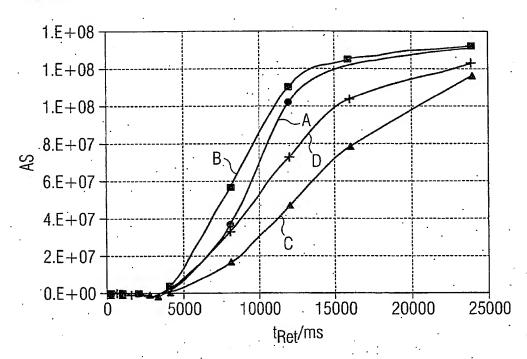
FIG 8



A: non-rotated wafer

B: non-rotated wafer -10% storage capacitance

C: rotated wafer

-+- D: rotated wafer -10% storage capacitance